

Parikh 2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants(s): Parag Parikh  
Case: 2  
Serial No.: 10/770,046  
Filing Date: February 2, 2004  
Examiner: Unassigned  
Group: Unassigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *John Mauri* Date: April 14, 2004

Title: Clock Generation Circuits Providing Slewing of Clock Frequency

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

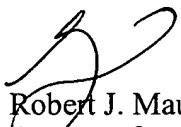
Attention: Official Draftsperson

Sir:

Applicants submit herewith nine (9) sheets of formal drawings in the above-referenced application.

Respectfully submitted,

Date: April 14, 2004

  
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